

Title:

EFFICIENT SPLITTING AND MIXING STREAMING-DATA FRAMES FOR PROCESSING THROUGH MULTIPLE PROCESSING MODULES

Inventor(s):

Raphael S. Lisitsa, *et al.*

Serial No.:

09/310,596

Docket No.:

MS99224.01/MSFTP1038US

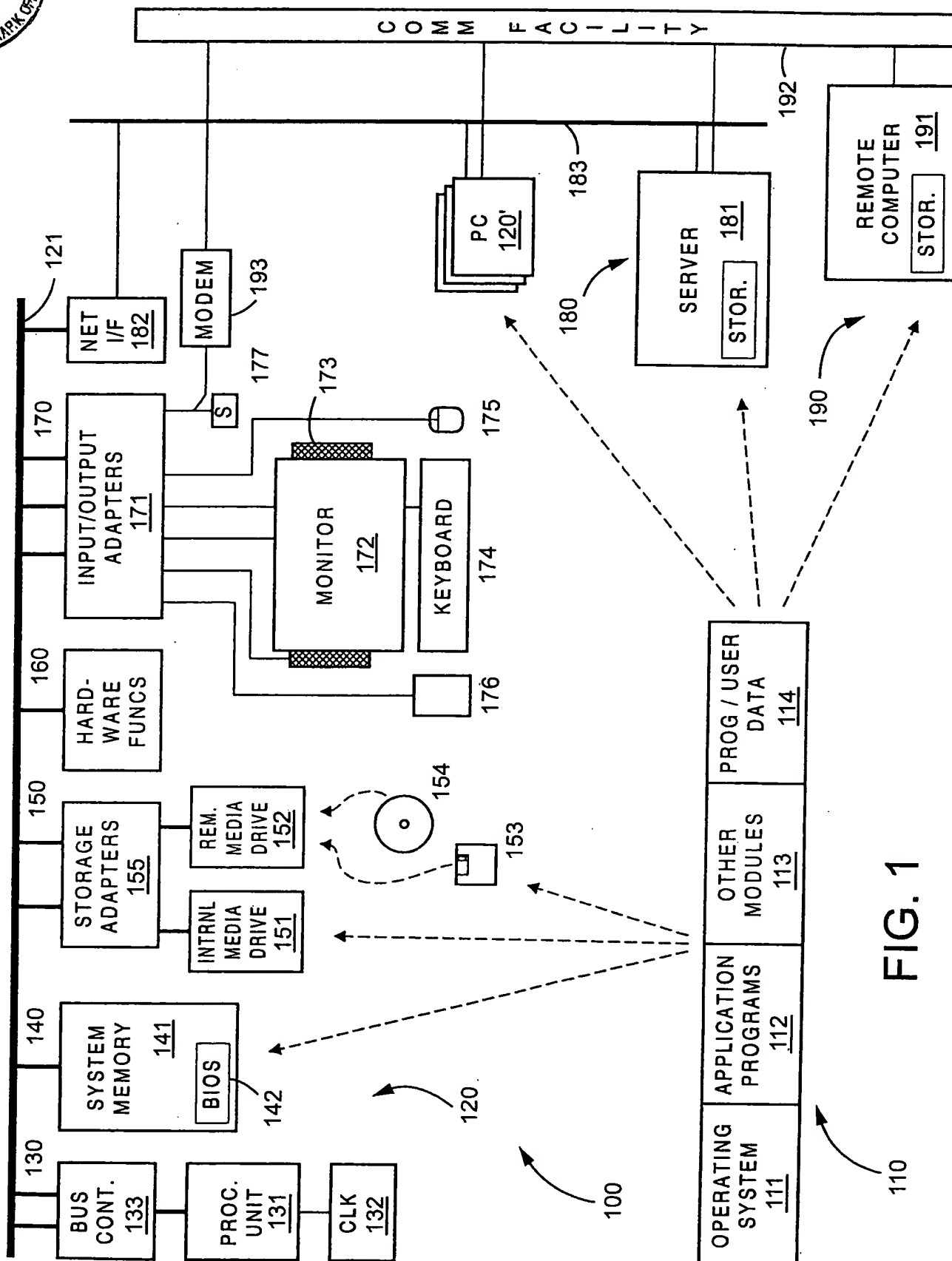
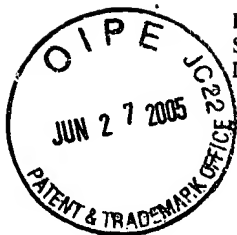


FIG. 1

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FIG. 3

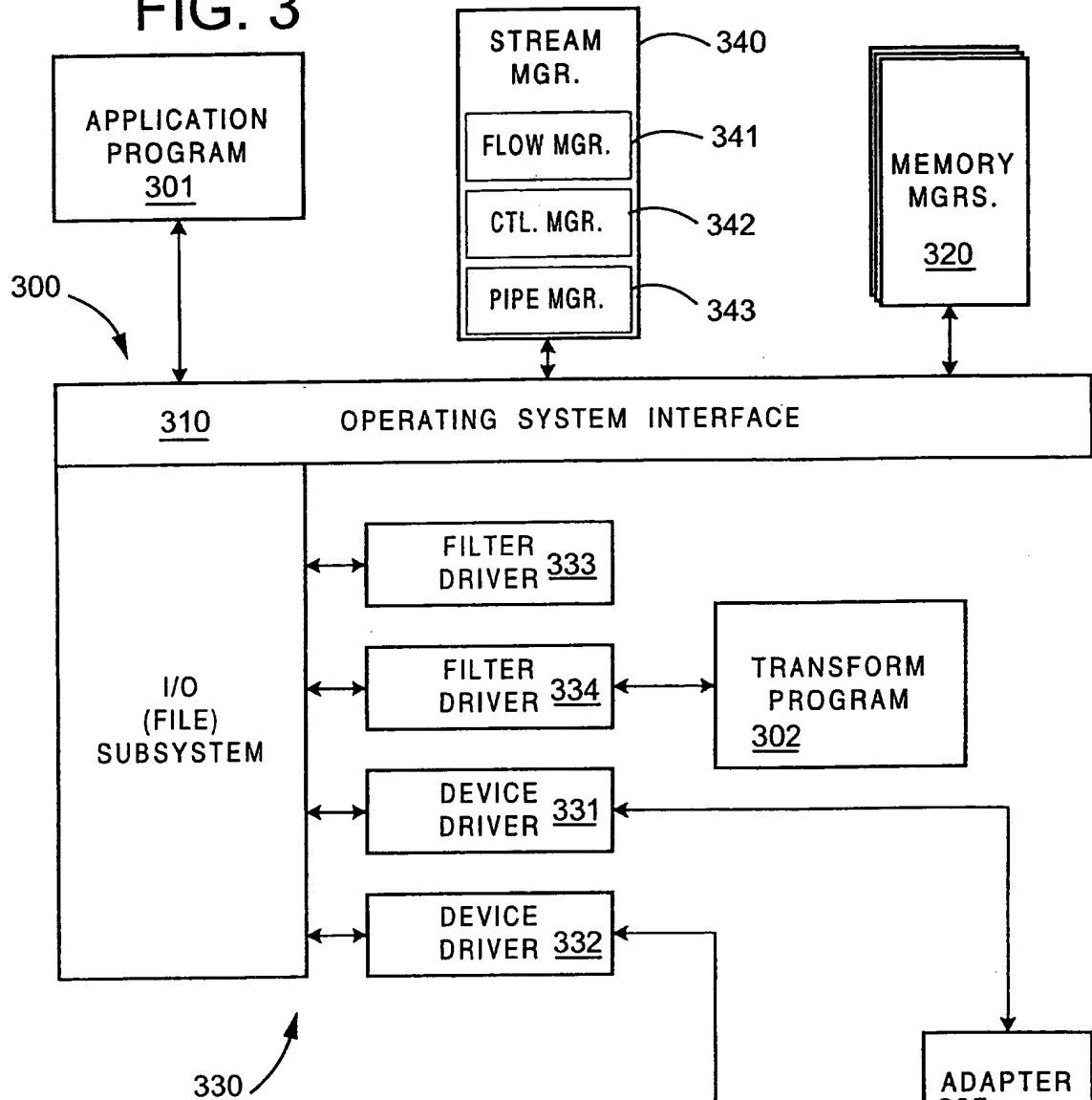
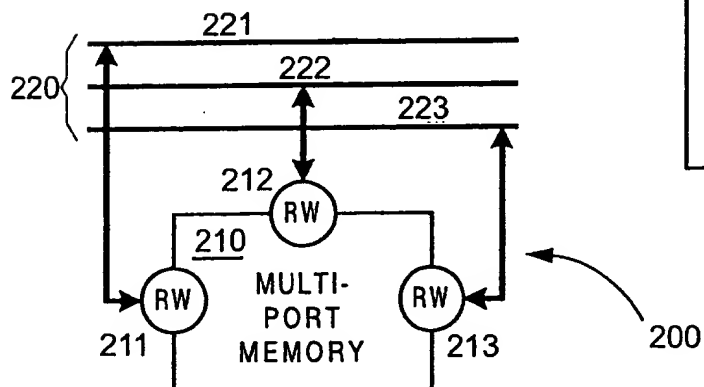


FIG. 2



Title:

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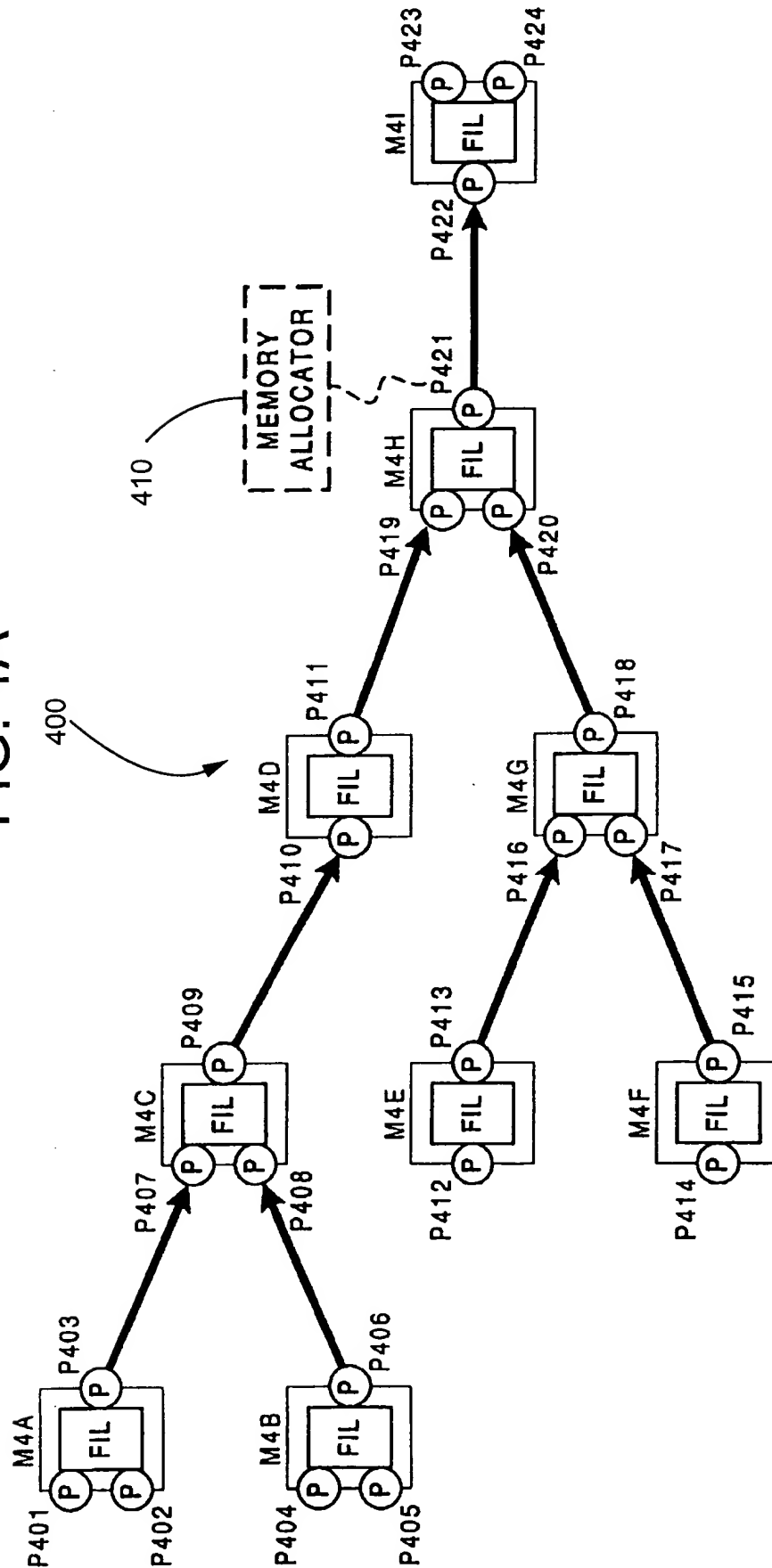
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FIG. 4A



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FIG. 4B

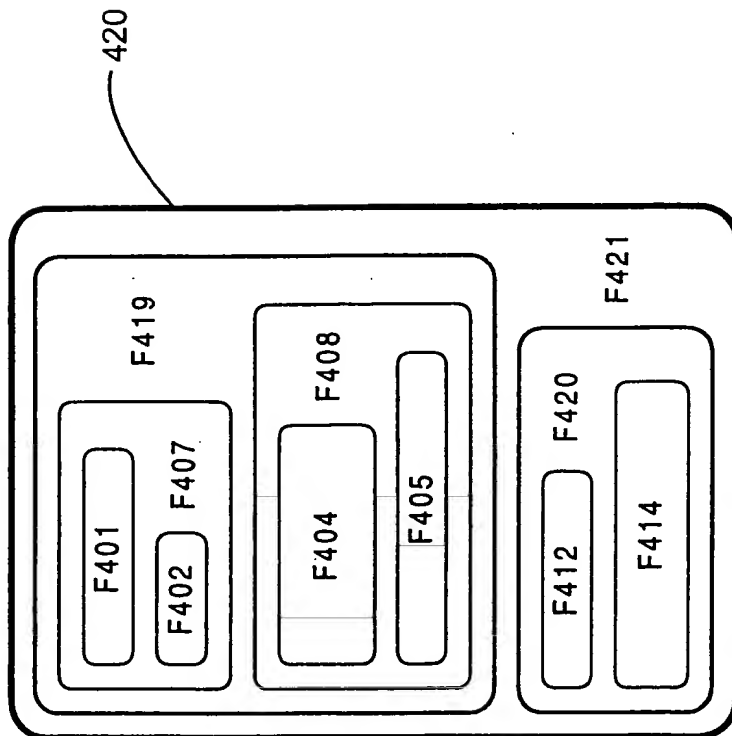
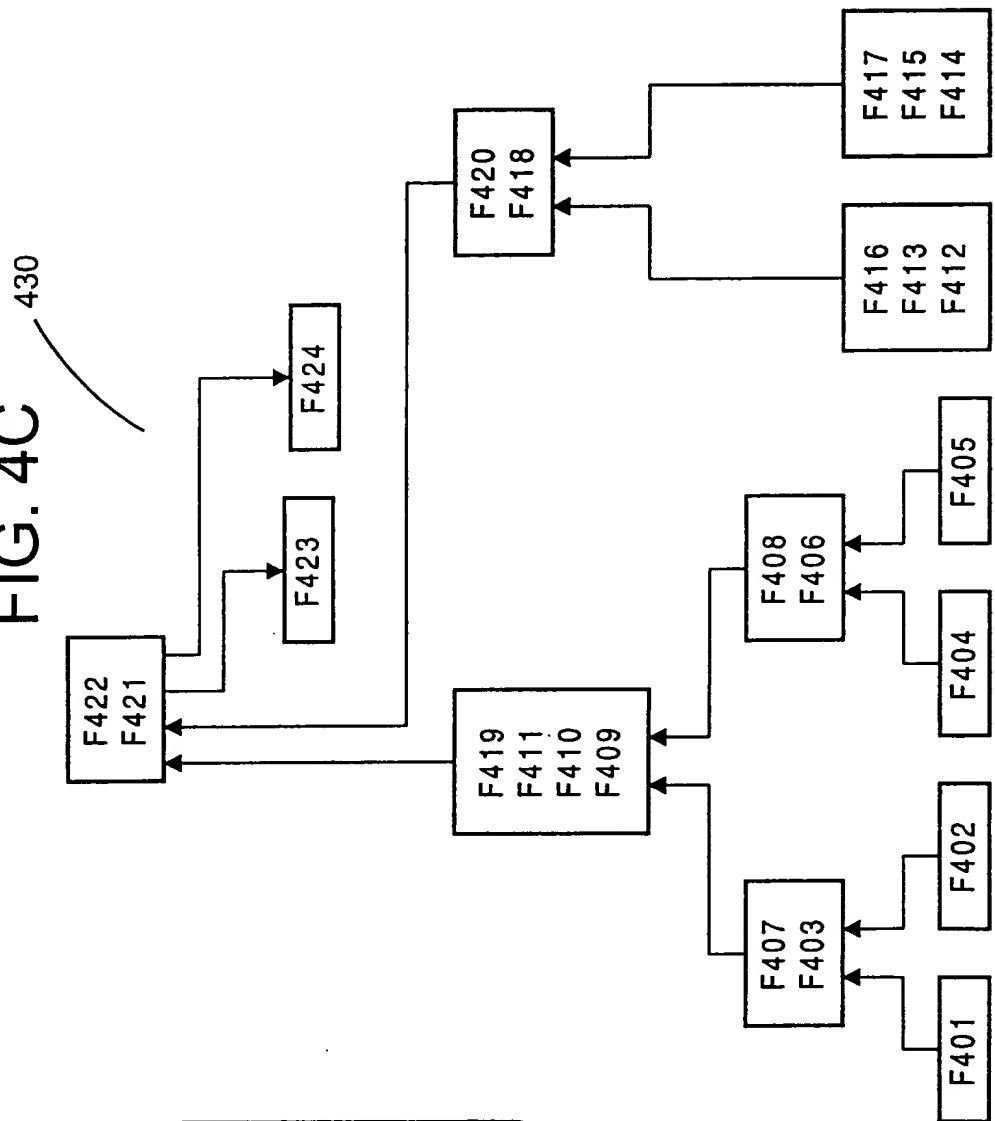


FIG. 4C

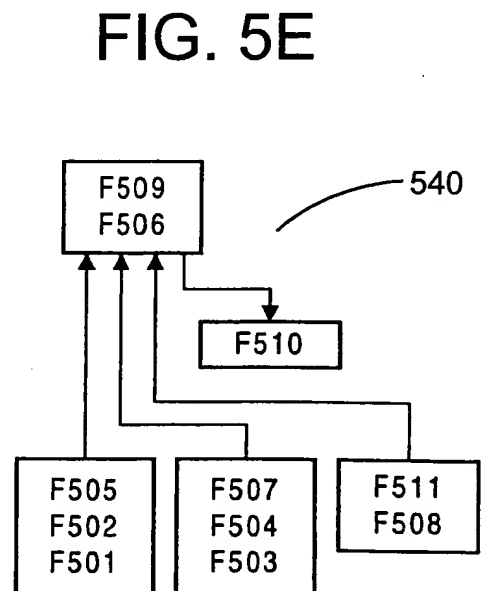
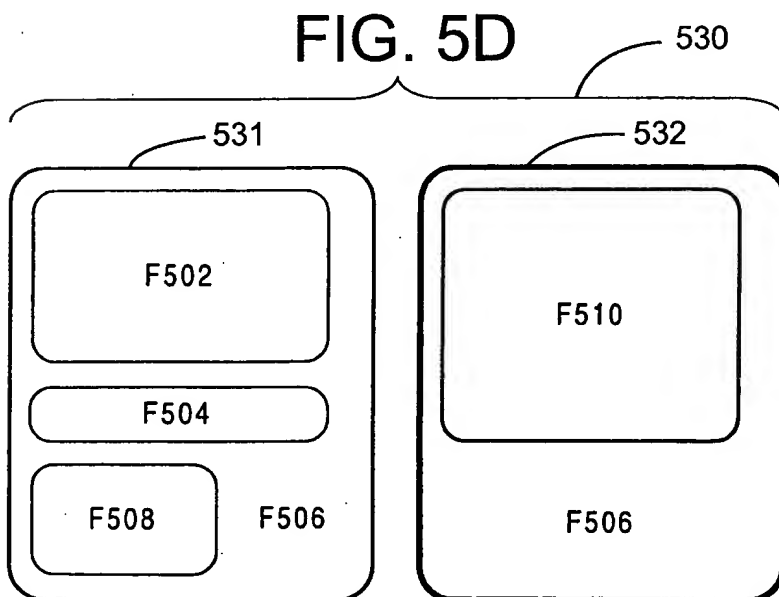
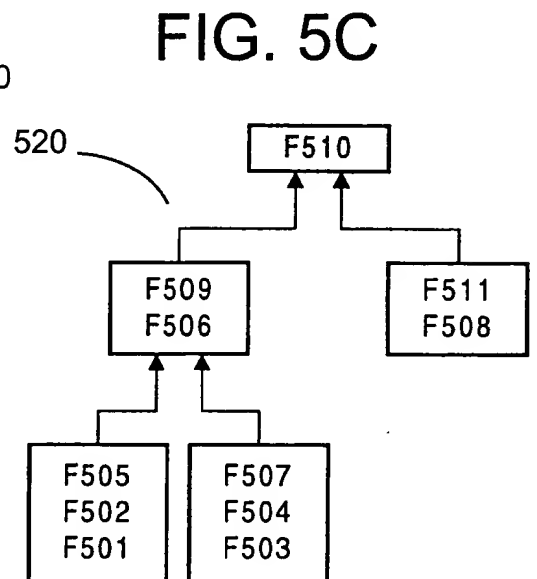
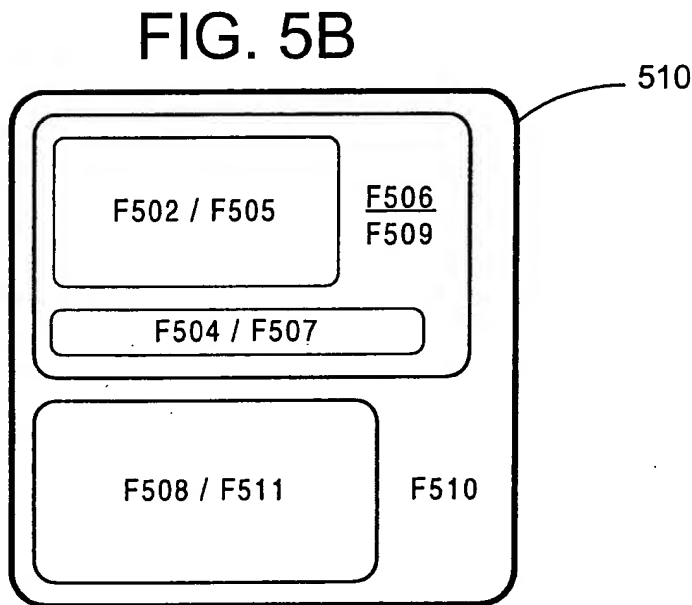
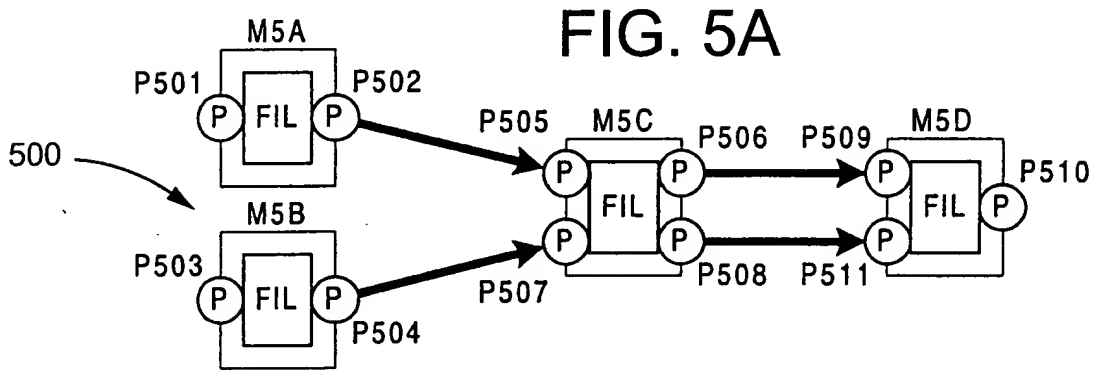


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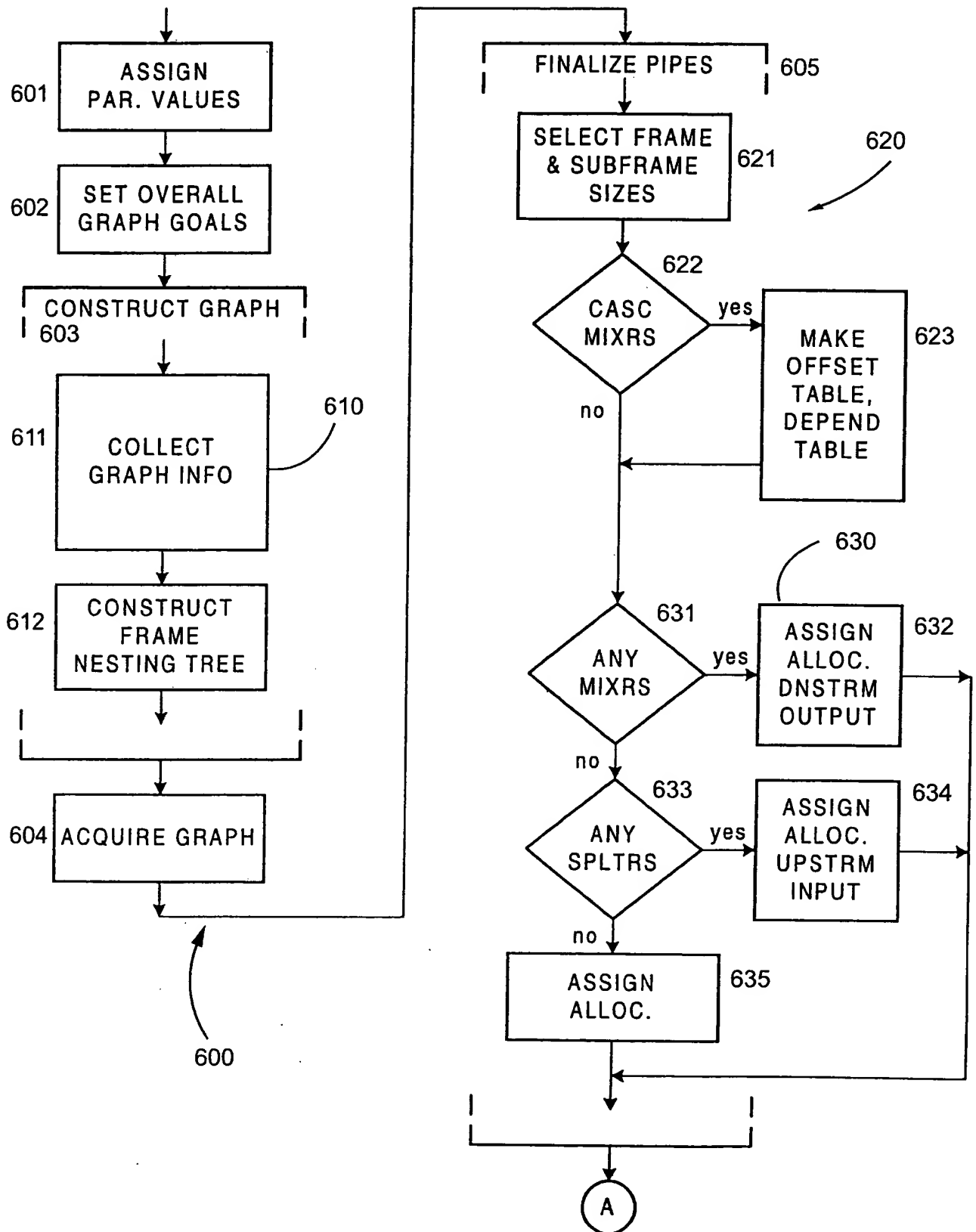
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FIG. 6A



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FIG. 6B

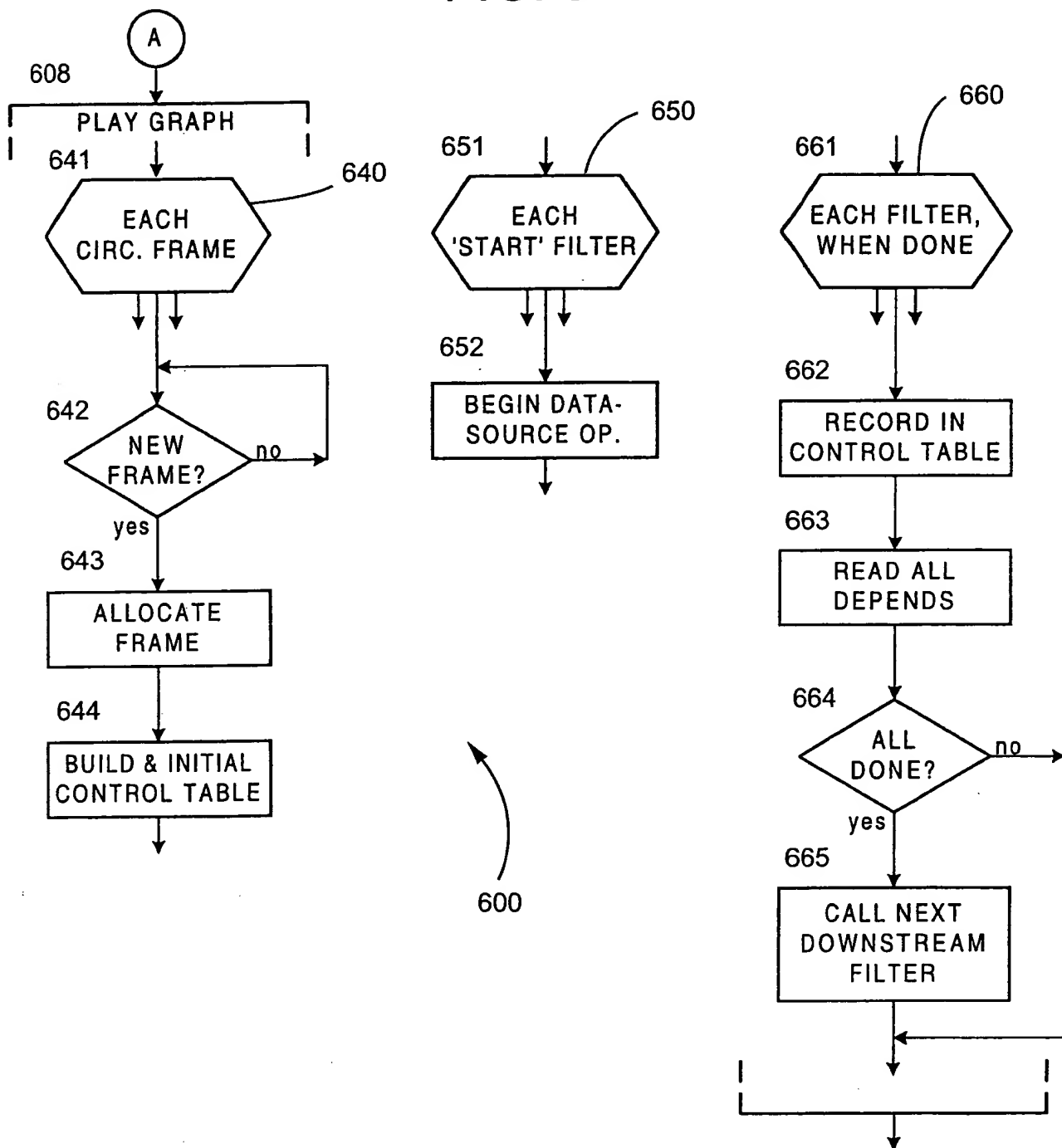


FIG. 7A

Pin ID	Offset from base of physical frame
P401	Offset #01
P402	Offset #02
P403	Offset #07
P404	Offset #04
P405	Offset #05
P406	Offset #08
P407	Offset #19
P408	Offset #08
P409	Offset #19
P410	Offset #19
P411	Offset #19
P412	Offset #16
P413	Offset #16
P414	Offset #17
P415	Offset #17
P416	Offset #16
P417	Offset #17
P418	Offset #20
P419	Offset #19
P420	Offset #20
P421	Offset #21
P422	Offset #21
P423	Offset #23
P424	Offset #24

FIG. 7B

Filter	Source
M4A	M4A, M4B M4E, M4F M4C M4D, M4G M4H
M4B	
M4E	
M4F	
M4C	
M4G	
M4D	
M4H	
M4I	

FIG. 8

